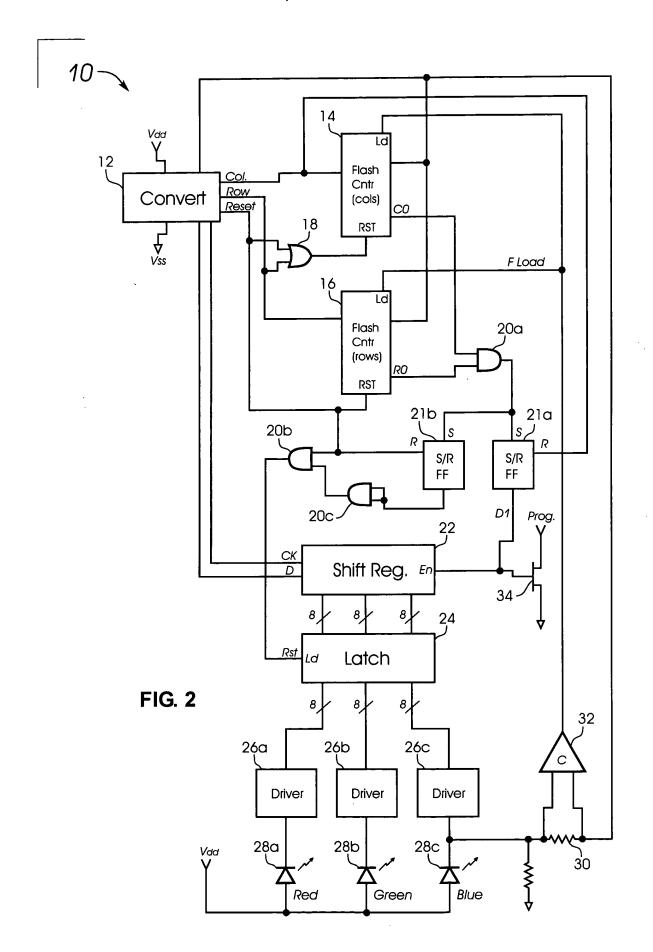


FIG. 1 (Prior Art)



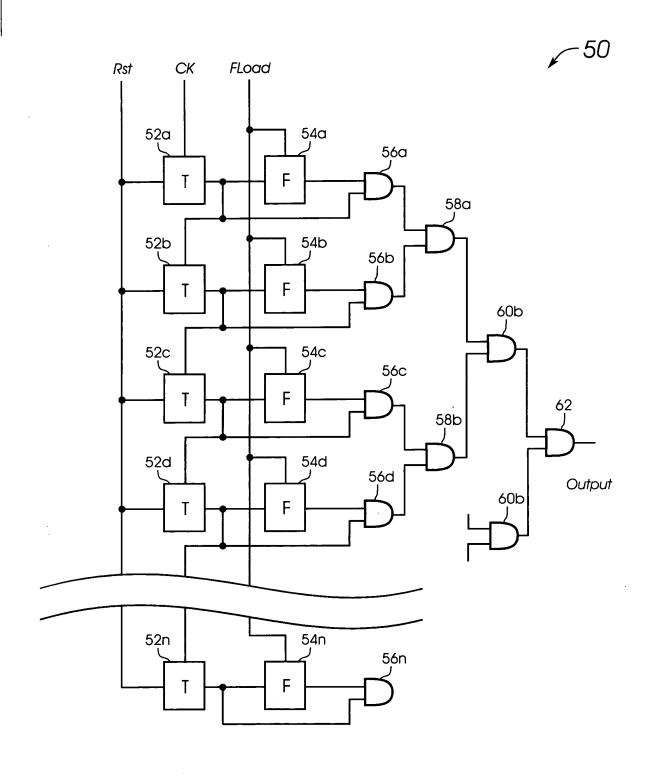
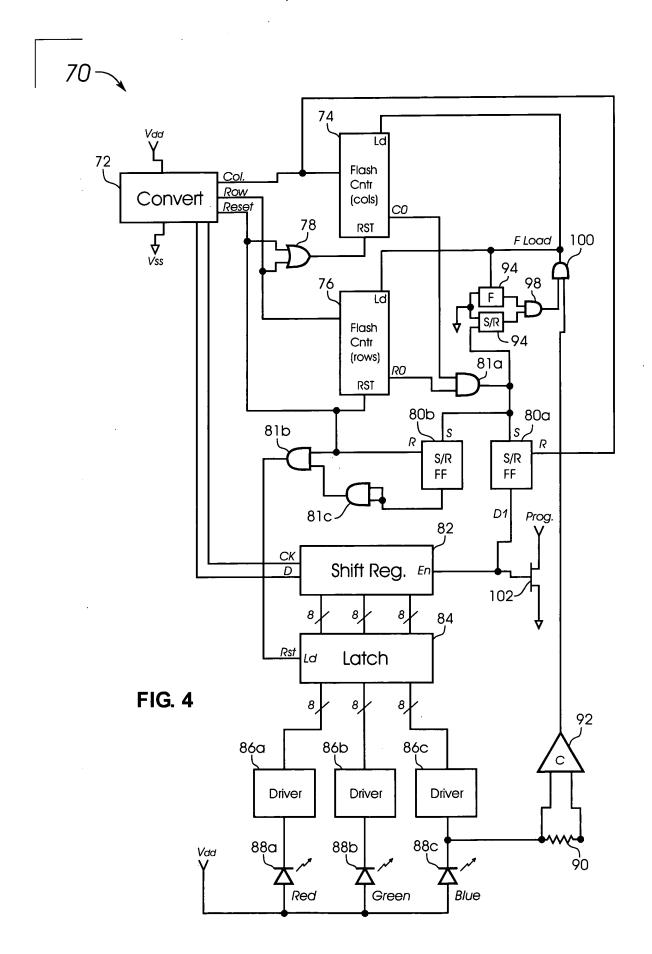


FIG. 3



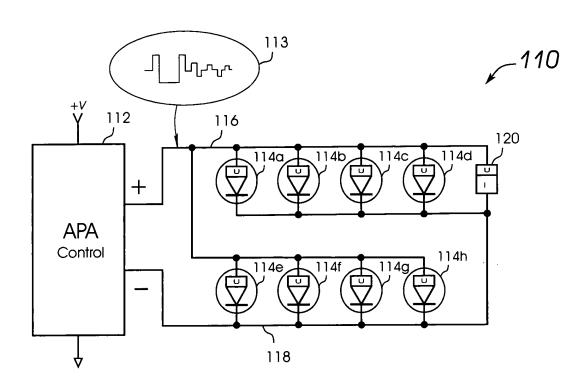


FIG. 5

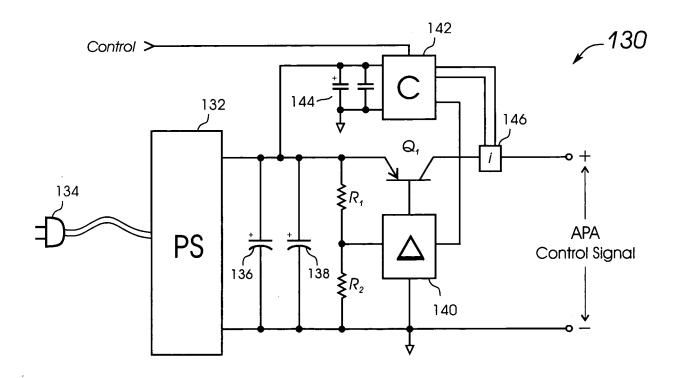


FIG. 6



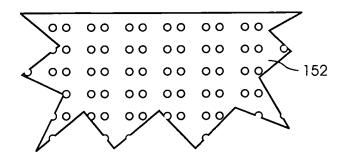


FIG. 7

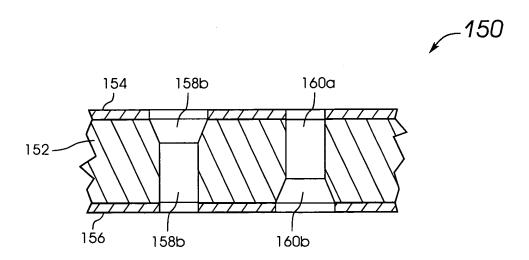


FIG. 8

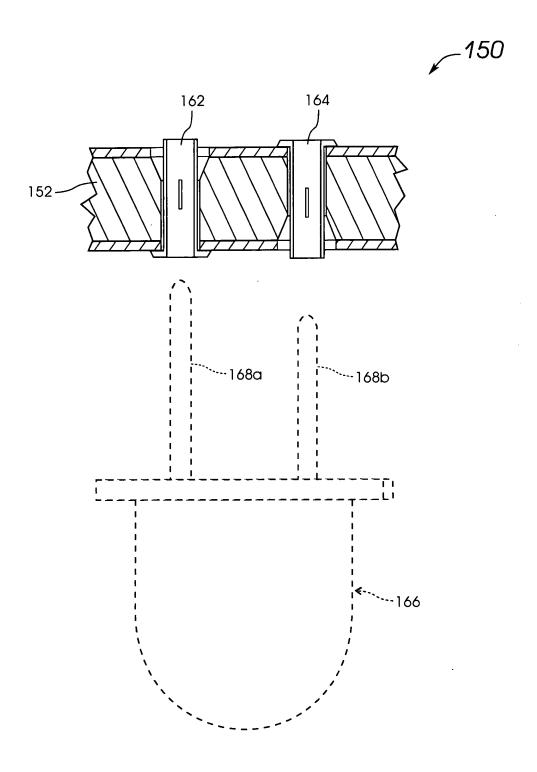


FIG. 9

- 170 Connecting in an array - 172 Detect & Enter Programming mode - 174 Keep address count - 176 NO Detect Program. Signal? YES - 178 Load array address into memory

End program mode

Operational mode

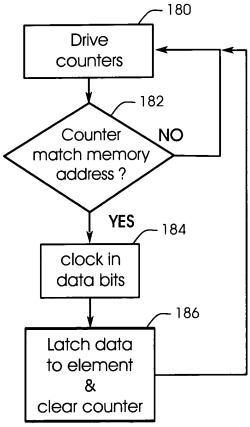


FIG. 10A

FIG. 10B